## Indian Institute of Technology Palakkad भारतीय प्रौद्योगिकी संस्थान पालक्काड

**Nurturing Minds For a Better World** 



14 August 2023

## Notification for Recruitment of Junior Research Fellow, Electrical Engineering Project funded by Chips to Startup Program

## Ref: IITPKD/2023/027/EE/SUD

- **Project Areas:** RF Active Antennas over the Ultra-wideband. Work will involve design, fabrication and characterization of antenna integrated with RF amplifier over the UWB regime.
- Required Skills:
  - a. Background in RF Amplifier Designs including Antennas.
  - b. Exposure in HFSS/CST and ADS
- Eligibility: B.E./ B.Tech in Electronics and Communication Engineering with minimum of 60% marks and having valid GATE score (or) M.E./ M. Tech in Electronics Engineering with minimum of 60% marks and having valid GATE score.
- Age: Candidates who are not exceeding 31 years of age, as on the closing date of application, with relaxation to candidates belonging to OBC/ SC/ ST/ PWD categories and women applicants as per Government of India norms.
- **Appointment Duration:** Initially for six months with the possibility of an extension depending on performance and availability of funding
- No. of Positions: One
- Salary Particulars: The monthly remuneration for this position will be Rs. 37,000/- (Thirty-Seven Thousand Rupees Only). Hostel accommodation may be provided at the institute based on availability on a chargeable basis. In case of non-availability of institute hostel, admissible HRA will be provided as per Government of India rules.
- **Deadline for application**: 18 September 2023 (Monday)

Candidates satisfying the required skills may send their resumes and certificates pertaining to educational qualifications starting from class X (in a single PDF) to "sdey28@iitpkd.ac.in". The subject of the e-mail application should be **Application for Junior Research Fellow - IITPKD/2023/027/EE/SUD.** The candidate is expected to join immediately after receiving the offer letter.

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