



18 August 2025

Advertisement for Post-Doctoral Fellow (PDF) Recruitment
under 'Visvesvaraya PhD Scheme for Electronics and IT: Phase II, Digital India Corporation,
Ministry of Electronics and Information Technology (MeitY), Government of India
Ref: IITPKD/2025/035/EE/MSM

Indian Institute of Technology Palakkad invites applications from highly motivated and research-oriented scholars for a full-time Post-Doctoral Fellow (PDF) position under the Visvesvaraya Scheme, supported by the Ministry of Electronics and Information Technology (MeitY). This PDF scheme is a prestigious and competitive research opportunity for early-career researchers to contribute to India's national missions in the ESDM and IT/ITES sectors with a focus of developing the products and creating intellectual property based on the research findings in the domains of ESDM and IT/ITES sectors. The PDF applicant must have a proven track record in Electronics System Design, Signal and Image Processing and VLSI Architectures for AI/ML/DL as a part of the full-time PhD thesis work established through their reputed research publications and recognitions.

A. Fellowship Benefits and Minimum Requirements

Fellowship	Rs. 1.08 Lakh per month (for 1 year) Direct disbursement by MeitY
PDF Contingency	Rs. 1.0 Lakh for the year as per the funding agency
Travel Support	Up to Rs. 2.10 Lakhs is available, for which the prior approval needs to be obtained from PhD Cell, as per the funding agency
Minimum Requirements	<ul style="list-style-type: none">▪ PhD degree from reputed institutions/universities▪ Ph.D. degree in any areas of Electronics System Design, Signal and Image Processing, and VLSI Architectures for AI/ML/DL▪ The PDF applicant should have completed PhD within the past 5 years on the last date of submission of application▪ Candidates should provide evidence of having conducted high-quality research during or after their Ph. D. program with at least 02 (two) Q1 journal publications in SCI / SSCI / SCIE indexed journals and/or patents
Tenure	The position is temporary for one year.
Age	Not more than 35 years as of the closing date of the advertisement.
Preferable	<ul style="list-style-type: none">▪ The candidate must be able to join the PDF position within one week from the date of the offer letter.
Time Schedule of Interview Process	Last date of Online Application Submission: 27th August 2025 Tentative Schedule For Interview: 29 th August 2025
How to Apply	Interested candidates may apply through the online link https://forms.gle/7ThEcYjDkM1iPWYv5
Contact for Queries	Email: msm@iitpkd.ac.in (Nodal Officer)

Note: Please read the Application Checklist, Eligibility Criteria and Duration, Responsibilities of the PDF Awardee, selection process, and other points included with this advertisement
Sending resumes/ CVs by email will not be considered as a complete application.

B. Application Checklist

Candidates must submit the following documents (in a single PDF file):

1. Detailed CV with Photo
2. Submit the educational certificates and mark sheets/grade cards of:
 - Secondary (10th)
 - Higher Secondary (12th)
 - All Degree Certificates (UG/PG/PhD)
3. Submit a summary of Ph.D. work (**one page**)
4. Submit a research proposal write-up defining clear objectives, outcomes, and deliverables against the award (**maximum two pages**) in the domains of Electronics System Design and VLSI Architectures for Signal and Image Processing and Machine Learning.
5. Submit list of journal publications with complete information, including the name of the authors, title of the paper, name of the journal, is it published in Q1 SCI / SSCI / SCIE indexed journals, and Digital Object Identifier of the paper.

C. Eligibility Criteria and Duration as per the Funding Agency

- a) The PDF applicant should be an Indian citizen. The PDF is awarded to researchers with a proven academic record, including peer-reviewed publications and recognitions.
- b) The PDF applicant must have obtained a PhD degree from a recognized University/Institution with first class (in terms of grades, etc.) in all preceding levels and a good academic record throughout.
- c) Age limit: The upper age limit for the fellowship is 35 years on the date of submission of application.
- d) PDF applicants currently in regular employment will not be considered. However, scientists or researchers with temporary positions in academia or research institutions would be considered, but they would be required to relinquish their current roles if selected for the fellowship.
- e) The PDF applicants should not have completed their PhD at IIT Palakkad.
- f) PhD awarded within the last 5 years.
- g) Duration: The fellowship is purely a temporary assignment, and is tenable for a period of 01 year.
- h) The research proposal of the PDF applicant, to be submitted with application form, should define clear objectives, outcomes and deliverables against the award.
- i) Preference would be given to the PDF applicants who have active industry linkages and their research proposal is directed towards industry specific problems and aiming to develop products.
- j) Applicants must have published at least 02 (two) Q1 journal publications in SCI / SSCI / SCIE indexed journals in the domains of Electronics System Design, Signal and Image Processing, and VLSI Architectures for AI/ML/DL.

D. Responsibilities of the PDF Awardee as per the Funding Agency

In addition to own research work, the awardees need to participate in activities under the Visvesvaraya PhD Scheme as assigned by PhD Cell such as:

- Evaluation of research work of PhD Candidates
- Mentoring of PhD candidates
- Participation in collaborative research under the scheme

- Participation in workshops and events
- Other assignments by PhD Cell
- Provide research proposals including specific timelines, and deliverables
- Submit monthly progress reports and contribute to departmental research initiatives.
- Report significant achievements during the tenure
- Promptly follow the directions of PhD Cell
- Acknowledge the scheme at all significant forums including publications/ achievements/recognitions etc. using the shared template by the PhD Cell
- Inform the PhD Cell at least one month in advance for any long-term leave

E. The following points are considered for the nature and duration of the fellowship support

- The fellows are not eligible to receive any other fellowship from any Government or Non-Government source during the tenure of the fellowship
- The fellow is expected to undertake the research objectives by himself/herself during the entire duration of the fellowship.
- Post-doctoral positions are non-transferable.
- The research work is to be carried out in the same eligible institution at which PDF seat is allocated and the fellow is enrolled.
- Just fulfilling the minimum eligibility criteria should not entitle a candidate to be selected.
- In case the post-doctoral fellow undertakes any full/part-time assignment, his/her PDF award under the scheme would be discontinued. j) PhD Cell, DIC; MeitY reserves the right to hold/ stop/ discontinue the fellowship at any stage, if
 - o Appropriate progress is not being made.
 - o Violation/ Non-compliance of guidelines.
 - o Any other reason, which is deemed appropriate by PhD Cell, DIC, MeitY.

F. Shortlisting and Selection Process

- The selection committee is responsible for the recommendation.
- The candidate called for interview shall make a presentation of his/her research plan before a selection committee.
- No TA/DA will be provided and Candidates should make their arrangements for the stay.
- Shortlisted candidates will be informed by email. Only selected candidates will be contacted.
- Meeting the minimum criteria does not guarantee an interview or admission. The committee may set higher shortlisting criteria.
- Preferable: The applicant with the research domains of the Electronics System Design, Signal and Image Processing, and VLSI Architectures for AI/ ML/ DL and aiming to develop products based research findings.
- Note: Meeting the minimum criteria does not guarantee an interview or admission. The committee may set higher shortlisting criteria.
